

Electronic Acknowledgement Receipt

EFS ID:	3452692
Application Number:	10570290
International Application Number:	
Confirmation Number:	5446
Title of Invention:	Intergrated circuit and a method of cache remapping
First Named Inventor/Applicant Name:	Adrianus Josephus Bink
Customer Number:	65913
Filer:	Mark Alan Wilson
Filer Authorized By:	
Attorney Docket Number:	NL031031
Receipt Date:	13-JUN-2008
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File Listing:

Document Number	Document Description	File Name	File Size(Bytes) /Message Digest	Multi Part /.zip	Pages (if appl.)

1	Request for Continued Examination (RCE)	NL031031_RCETrans_031308.pdf	159424 aa1e5202a61c5449e5e2e1ea578d5e66b334e4ba	no	1
Warnings:					
This is not a USPTO supplied RCE SB30 form.					
Information:					
2		NL031031_RCE_031308.pdf	94806 06ec0a5bb0ce2694416446d855b4d05877771d4e7	yes	9
Multipart Description/PDF files in .zip description					
Document Description			Start	End	
Amendment Submitted/Entered with Filing of CPA/RCE			1	1	
Claims			2	4	
Applicant Arguments/Remarks Made in an Amendment			5	9	
Warnings:					
Information:					
3	Fee Worksheet (PTO-06)	fee-info.pdf	8187 8e943cc0f0694cdff2b2201d387de1816577ea	no	2
Warnings:					
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Total Files Size (in bytes):				262417	
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